

1



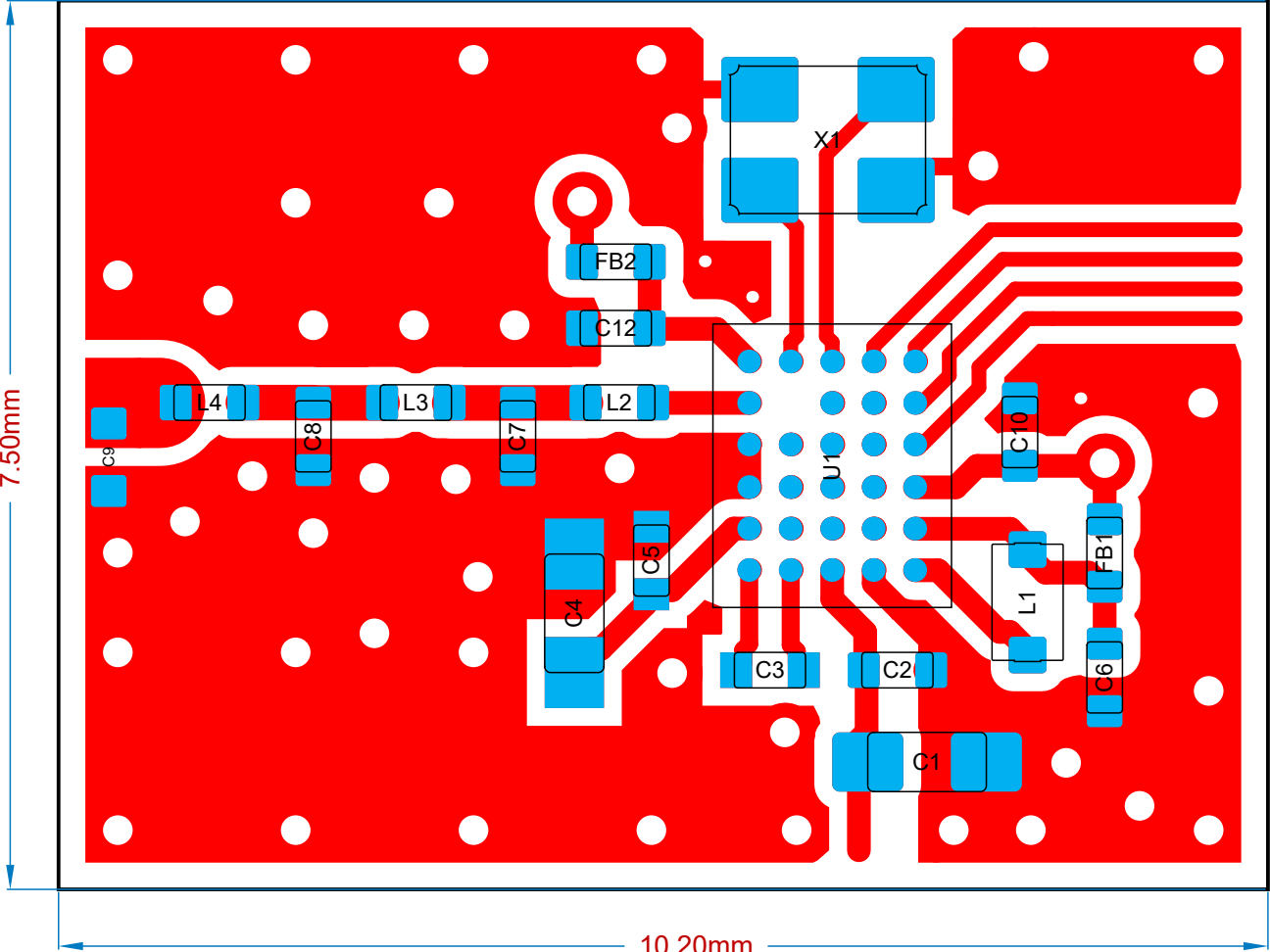
3

4



NORDIC
SEMICONDUCTOR

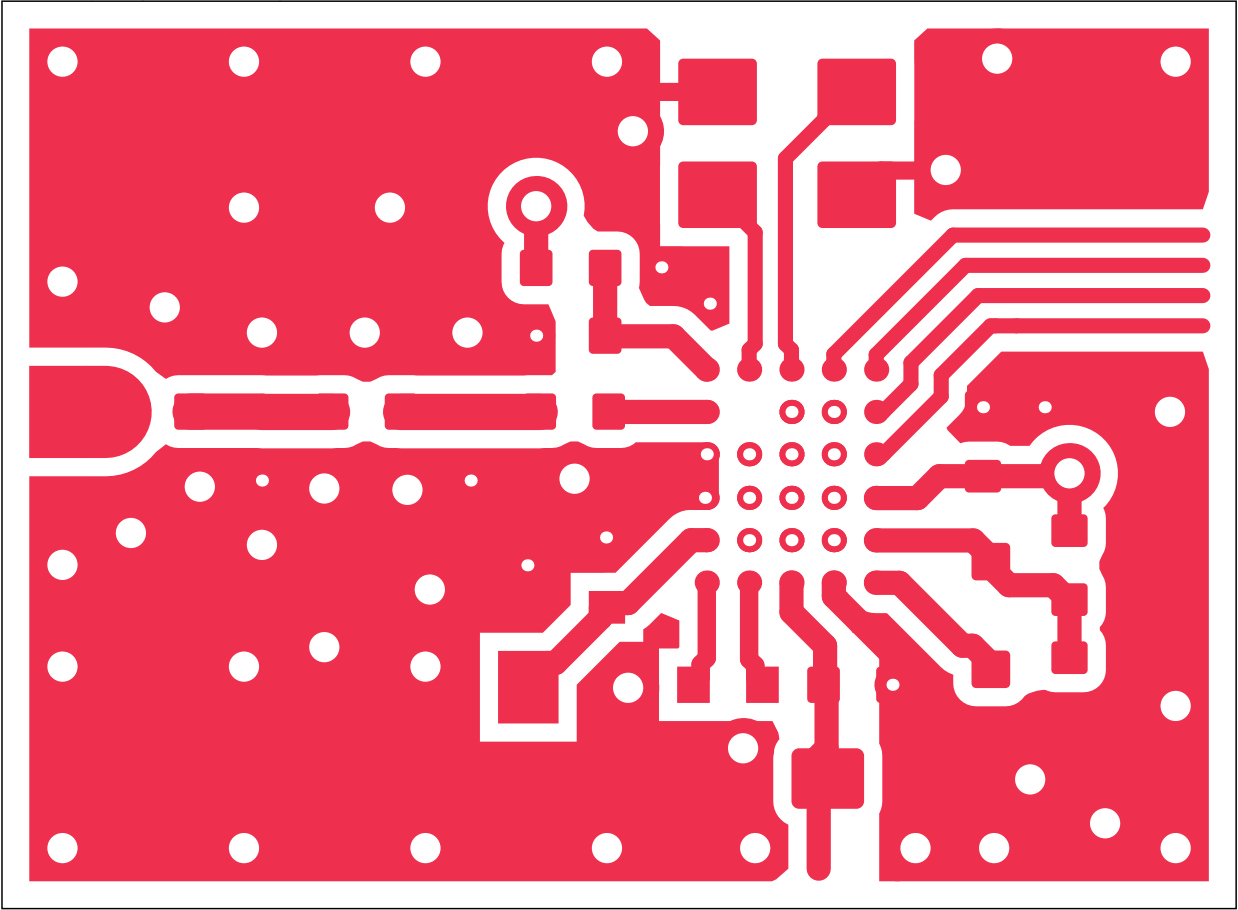
View from Top side (Scale 16:1)



Title			
1.2V to 1.7V VDDL Supply, Enabled DC/DC - 6 Layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LV10A-CAAA	Revision: 0.7
Date: 2026.09.21			Sheet 2 of 11
File: nRF54LV10A-CAAA_6L_config1.PCBDwf			Drawn By: Nordic Semiconductor
Classification: Public			



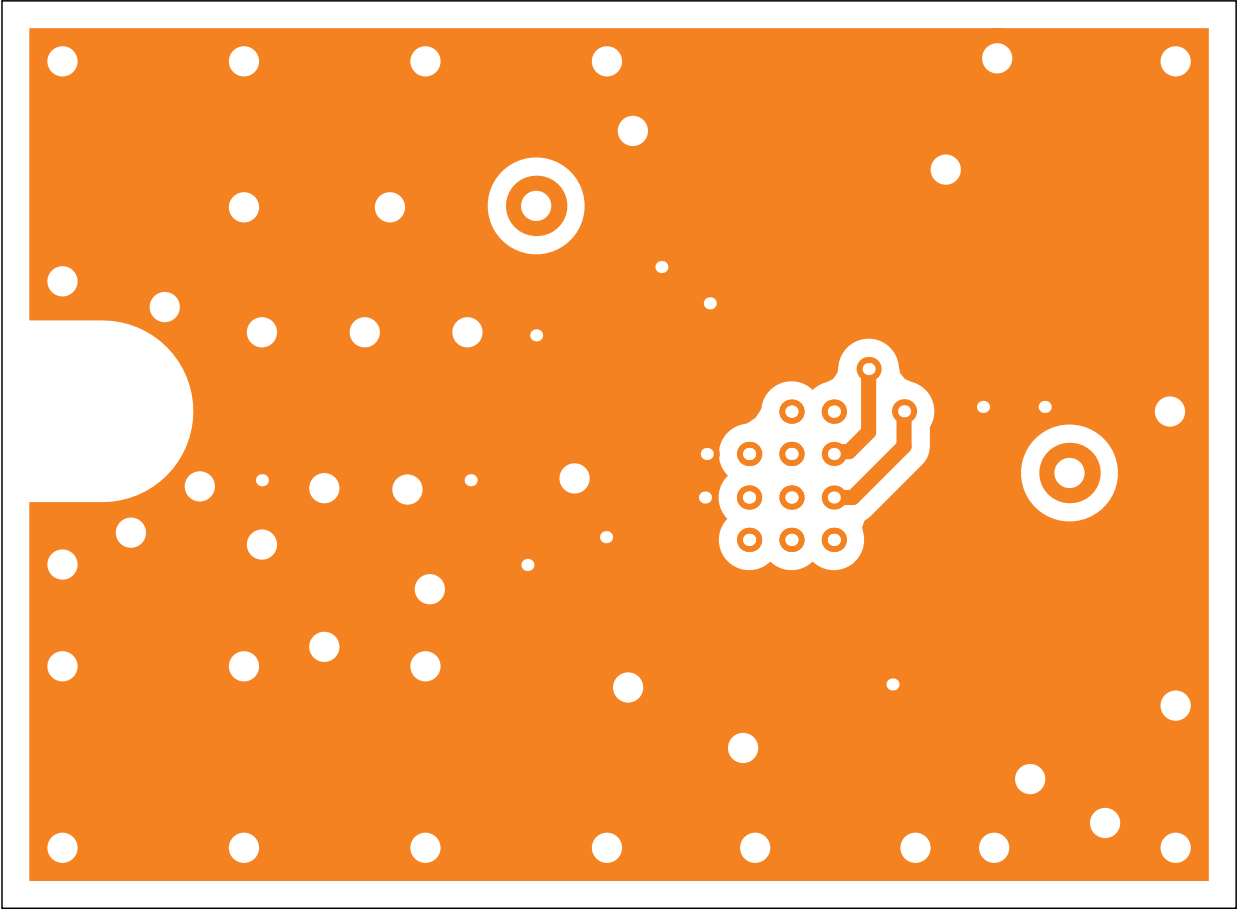
Top Layer (Scale 16:1)



Title			
1.2V to 1.7V VDDL Supply, Enabled DC/DC - 6 Layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LV10A-CAAA	Revision: 0.7
Date: 2026.09.21			Sheet 3 of 11
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Mid-Layer 1 (Scale 16:1)



Title			
1.2V to 1.7V VDDL Supply, Enabled DC/DC - 6 Layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LV10A-CAAA	Revision: 0.7
Date: 2026.09.21			Sheet 4 of 11
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Classification: Public			



A

B

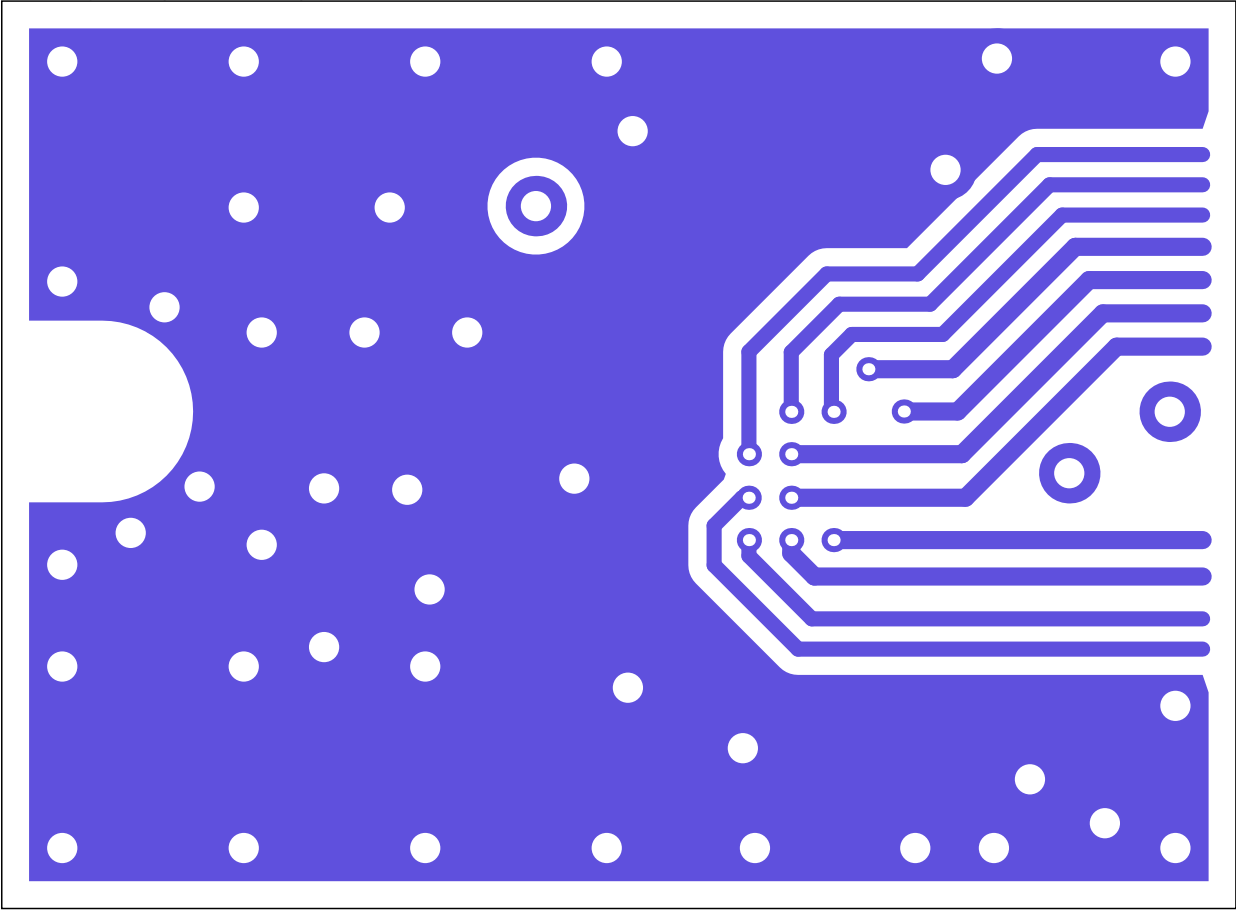
C

D

E

F

Mid-Layer 2 (Scale 16:1)



Title			
1.2V to 1.7V VDDL Supply, Enabled DC/DC - 6 Layer PCB			
Size:	A4	Config. No.:	1
Date:	2026.09.21	Device:	nRF54LV10A-CAAA
File:	nRF54LV10A-CAAA_6L_config1.PCBDwf	Revision:	0.7
Classification:	Public	Sheet	5 of 11
		Drawn By:	Nordic Semiconductor



A

B

C

D

E

F

A

B

C

D

E

F

1

1

2

2

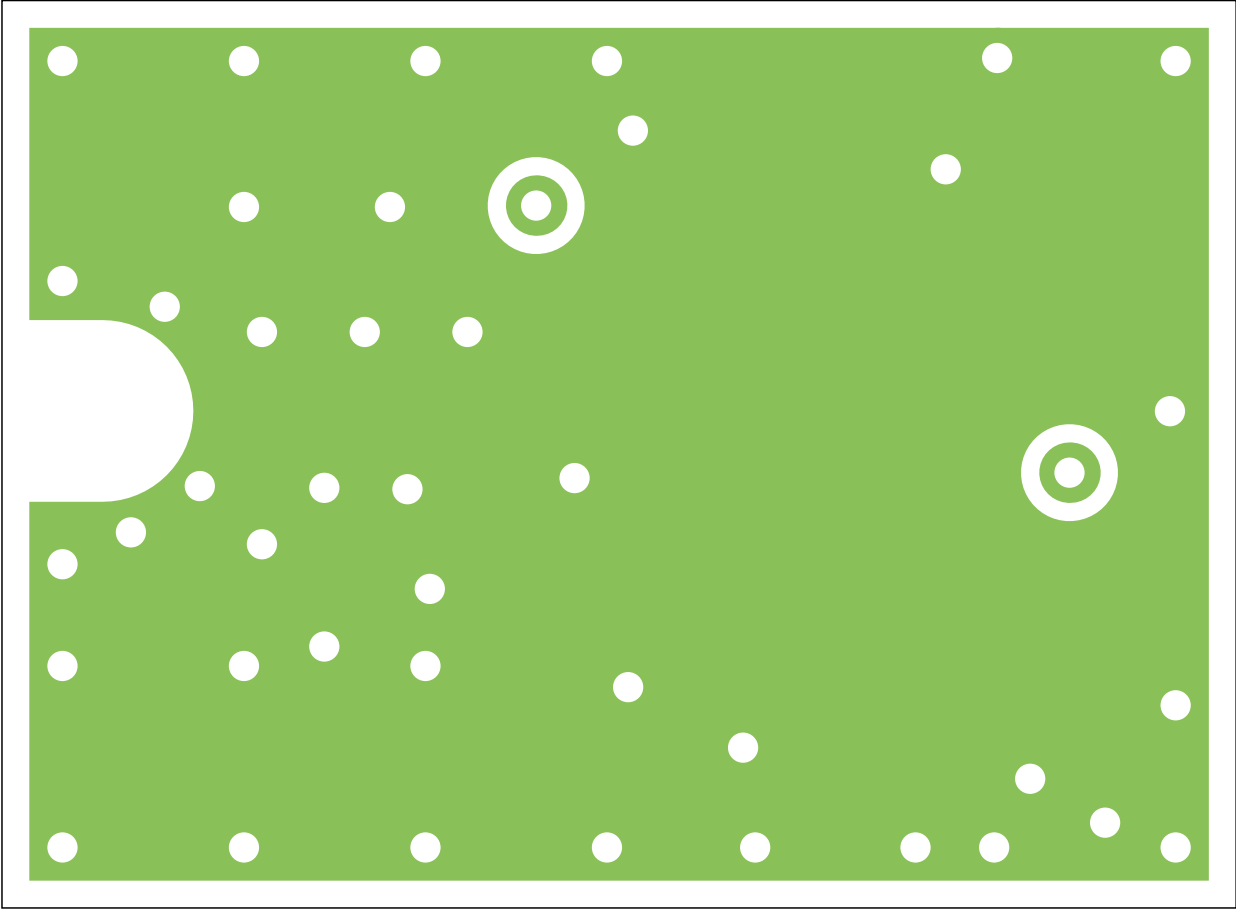
3

3

4

4

Mid-Layer 3 (Scale 16:1)



Title				
1.2V to 1.7V VDDL Supply, Enabled DC/DC - 6 Layer PCB				
Size:	A4	Config. No.:	1	
Date:	2026.09.21	Device:	nRF54LV10A-CAAA	
File:	nRF54LV10A-CAAA_6L_config1.PCBDwf	Revision:	0.7	
Classification:	Public	Sheet:	6 of 11	
		Drawn By:	Nordic Semiconductor	

A

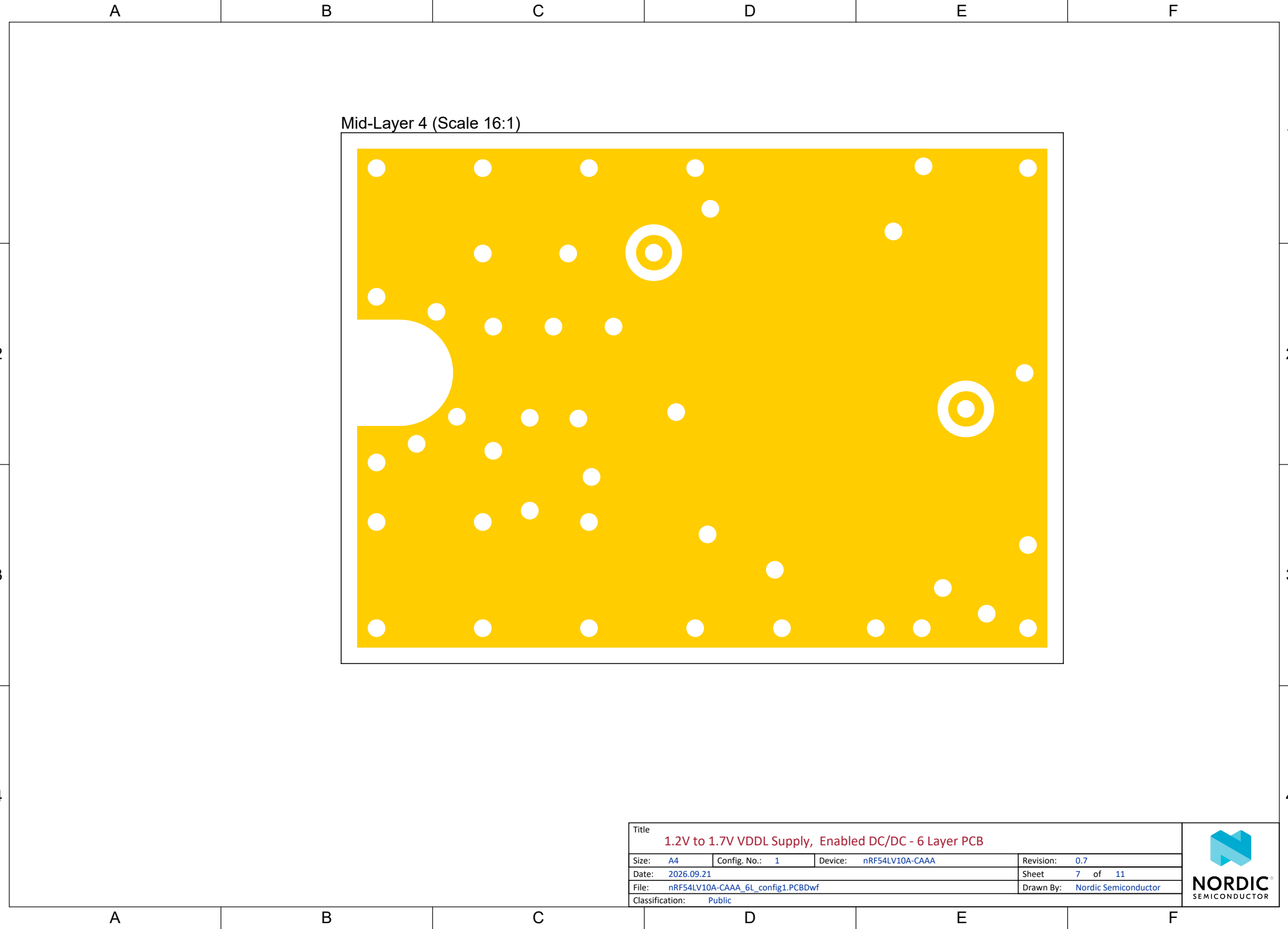
B

C

D

E

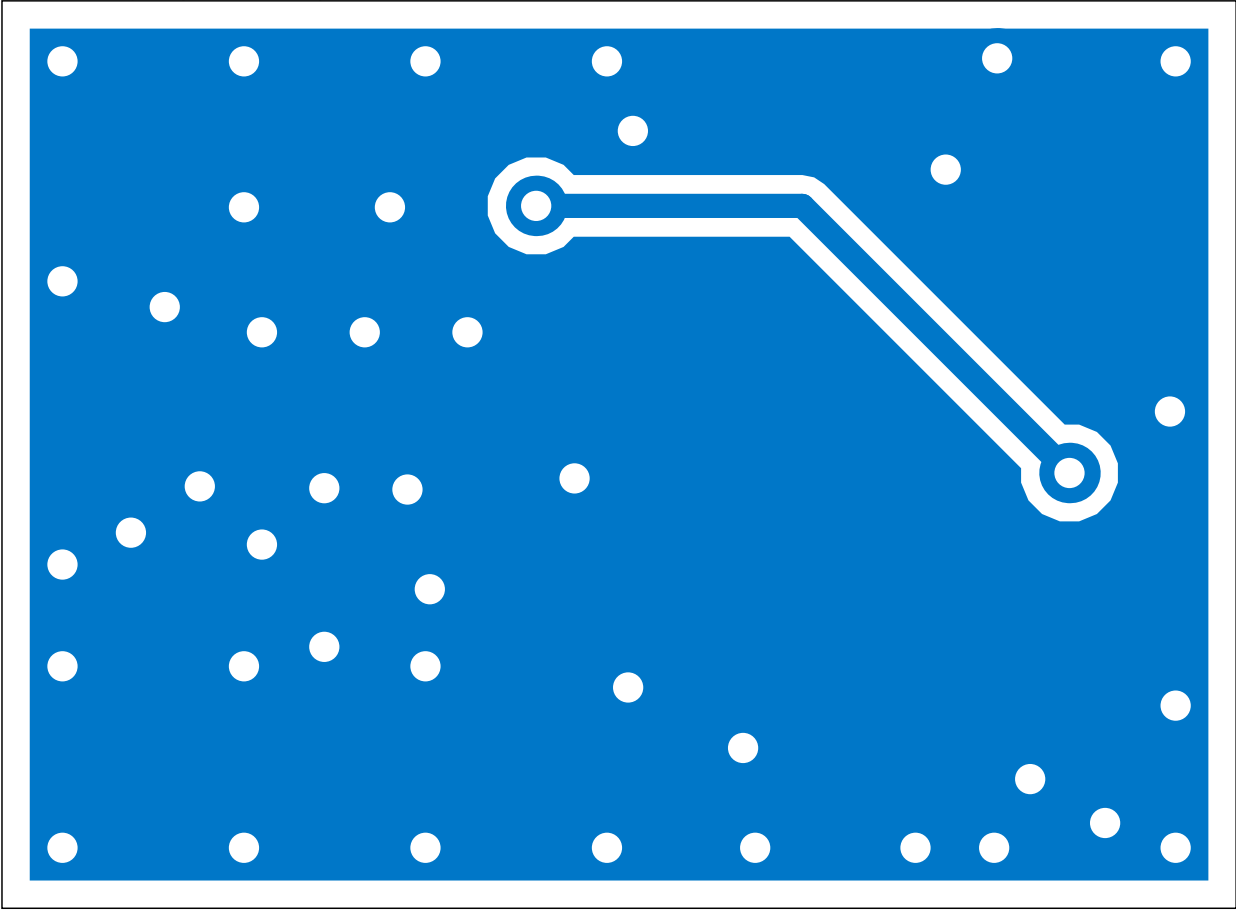
F



Title			
1.2V to 1.7V VDDL Supply, Enabled DC/DC - 6 Layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LV10A-CAAA	Revision: 0.7
Date: 2026.09.21			Sheet 7 of 11
File: nRF54LV10A-CAAA_6L_config1.PCBDwf			Drawn By: Nordic Semiconductor
Classification: Public			



Bottom Layer (Scale 16:1)



Title			
1.2V to 1.7V VDDL Supply, Enabled DC/DC - 6 Layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LV10A-CAAA	Revision: 0.7
Date: 2026.09.21			Sheet 8 of 11
File: nRF54LV10A-CAAA_6L_config1.PCBDwf			Drawn By: Nordic Semiconductor
Classification: Public			



Layer Stack Legend

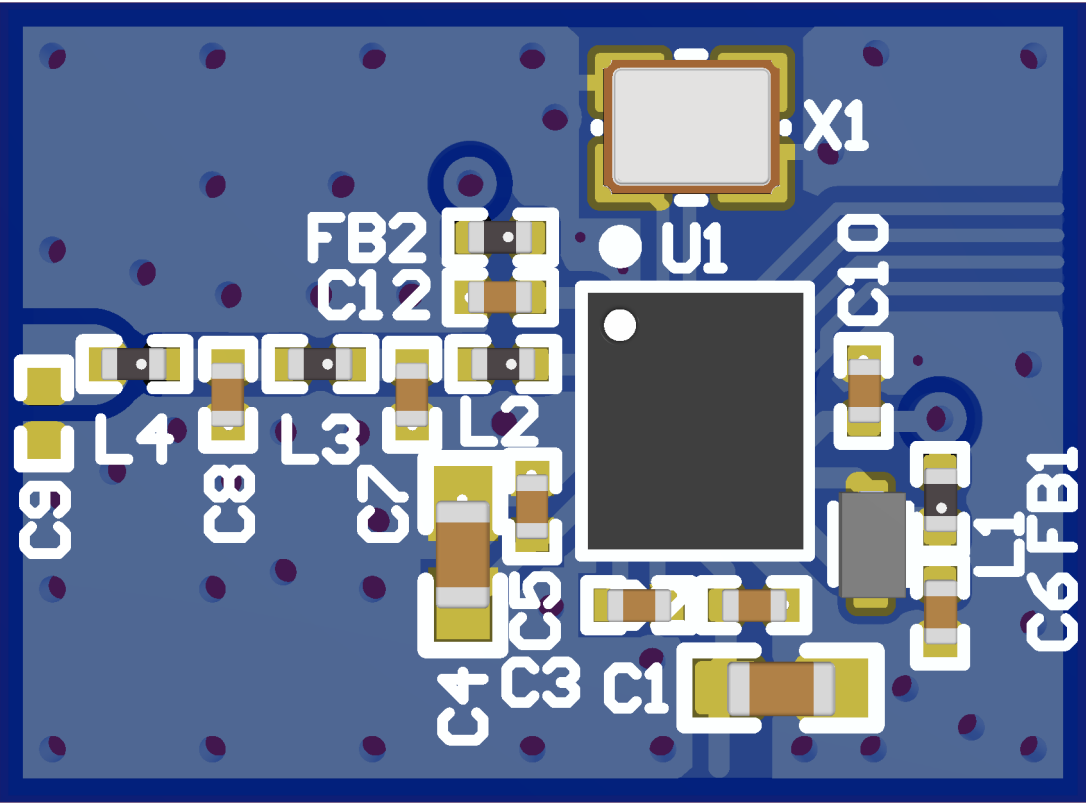
Material	Layer	Thickness	Type	Gerber
Surface Material	Top Overlay		Legend	GTO
Copper	Top Solder	0.01mm	Solder Mask	GTS
Prepreg	Top Layer	0.04mm	Signal	GTL
Copper		0.07mm	Dielectric	
Prepreg	Mid-Layer 1	0.04mm	Signal	G1
Copper		0.07mm	Dielectric	
Prepreg	Mid-Layer 2	0.04mm	Signal	G2
Copper				
Core		1.20mm	Dielectric	
Copper	Mid-Layer 3	0.04mm	Signal	G3
Prepreg		0.07mm	Dielectric	
Copper	Mid-Layer 4	0.04mm	Signal	G4
Prepreg		0.07mm	Dielectric	
Copper	Bottom Layer	0.04mm	Signal	GBL
Surface Material	Bottom Solder	0.01mm	Solder Mask	GBS
	Bottom Overlay		Legend	GBO

Total thickness: 1.71mm

Title 1.2V to 1.7V VDDL Supply, Enabled DC/DC - 6 Layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LV10A-CAAA	Revision: 0.7
Date: 2026.09.21			Sheet 9 of 11
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Classification: Public			

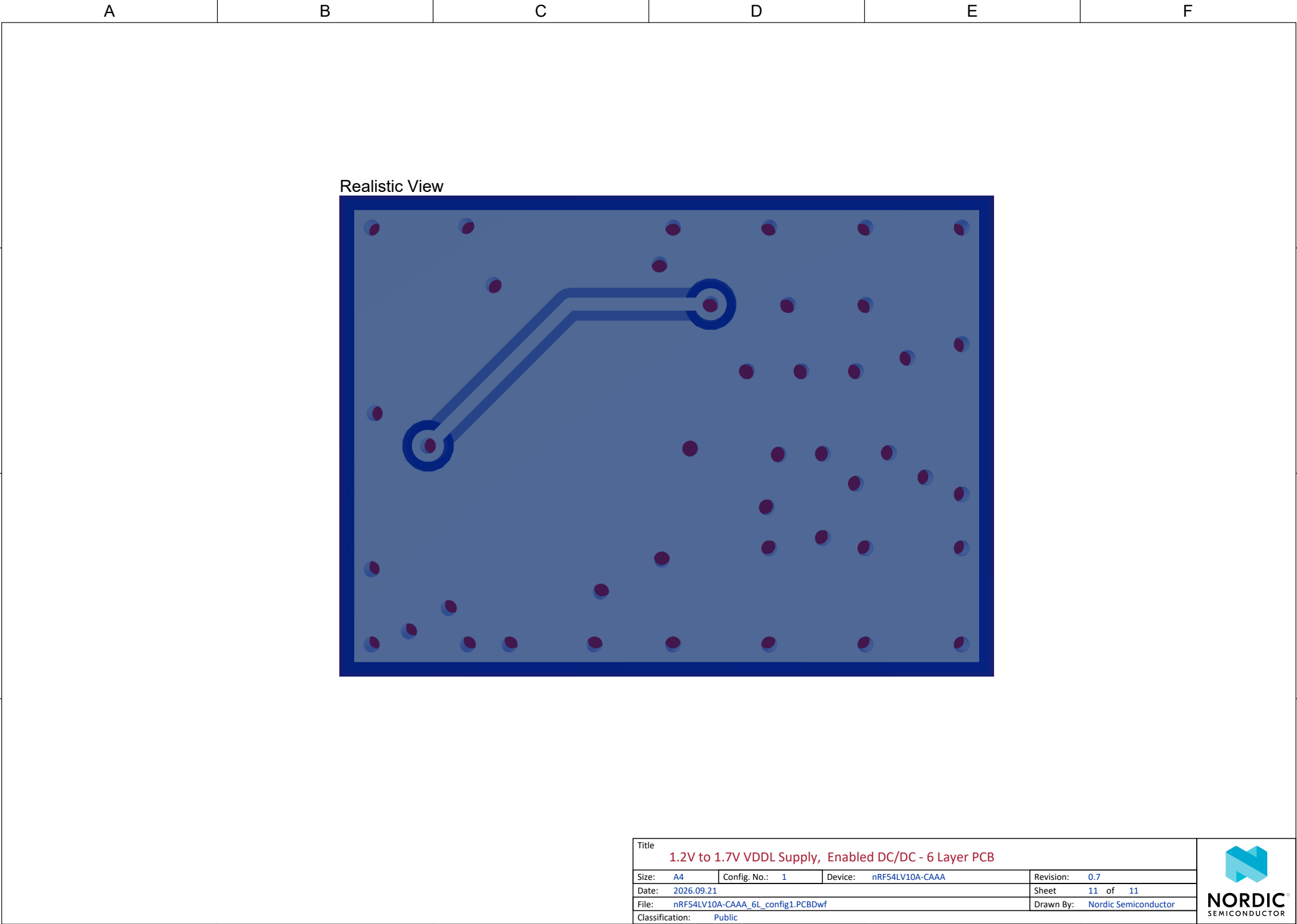


Realistic View



Title			
1.2V to 1.7V VDDL Supply, Enabled DC/DC - 6 Layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LV10A-CAAA	Revision: 0.7
Date: 2026.09.21			Sheet 10 of 11
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Classification: Public			





Title			
1.2V to 1.7V VDDL Supply, Enabled DC/DC - 6 Layer PCB			
Size: A4	Config. No.: 1	Device: nRF54LV10A-CAAA	Revision: 0.7
Date: 2026.09.21			Sheet 11 of 11
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Classification: Public			

